



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

INVENTORS:

Thomas J. Krutsick

CASE: 5

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TITLE: FIELD PLATED RESISTOR WITH ENHANCED  
ROUTING AREA THEREOVER

SERIAL NO. 09/650,604

GROUP ART UNIT 2826

FILING DATE 04/18/00

EXAMINER AHMED N. SEFER

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VA 22313-1450

**CORRECTION TO AMENDMENT**

SIR:

Following is a complete listing of the entire "Amendments to the claims"  
section of applicant's amendment filed December 20, 2006.

Claims 1-27 (canceled)

28. (currently amended) An integrated circuit having a field-plated  
resistor the field-plated resistor comprising:

a. a resistor body formed in a semiconductor substrate, the  
resistor body having first and second contact regions,